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Notice of Allowability   Description   Des		Application No.	Applicant(s)	
Michael B. Shingleton	Notice of Allowability	09/973,843	IWAI, TAISUKE	
The MAILING DATE of this communication appears on the cover sheet with the correspondence address-All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included networks the previously mailed, a holice of Alkowane (PTOL-85) or other appropriate communication will be mailed in due course, THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.31 and MPEP 1308.  1. ☑ This communication is responsive to the amendment dated 11-22-2004.  2. ☑ The allowed claim(s) is/are 1,2 and 4-28.  3. ☑ The drawings filed on 10-11-2001 are accepted by the Examiner.  4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  a) ☐ Image: a provide one of the priority documents have been received.  2. ☐ Certified copies of the priority documents have been received in Application No		Examiner	Art Unit	
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An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

In order to rejoin the withdrawn species the following obvious error concerning the dependency of the following claims have been corrected as follows:

- 4. (Currently Amended) The amplifier accordingly to claim [[3]] 2, wherein said rear stage DC bias control circuit comprises AC blocking inductors connected in series between the emitter and the base, and a hunt resistor.
- 6. (Currently Amended) The amplifier according to claim [[3]] 2, wherein said rear stage DC bias control circuit includes resistors connected in series between the emitter and the base, and a shunt resistor.
- 7. (Currently Amended) The amplifier according to claim [[3]] 2, wherein said rear stage DC bias control circuit includes an AC blocking inductor and a resistor connected in series between the emitter and the base, and a shunt diode.
- 8. (Currently Amended) The amplifier according to claim [[3]] 2, wherein said rear stage DC bias control circuit includes resistors connected in series between the emitter and the base, and a shunt diode.

Authorization for this examiner's amendment was given in a telephone interview with Sam Huang on 12-13-2004.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael B. Shingleton whose telephone number is (571) 272-1770. The examiner can

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normally be reached on Tues-Fri from 8:30 to 4:30. The examiner can also be reached on alternate Mondays. The examiner normally has the second Mondays of the bi-week off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal, can be reached on (571)272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

MBS December 13, 2004

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